

The diagram illustrates a system for generating an analog signal from a digital input. The system is divided into two main sections: a digital processing block (10) and a laser output block (32).

Block 10 (Digital Processing):

- STR (NRZI SIGNAL):** The primary digital input signal.
- WAVEFORM CONTROL TABLE (16):** Receives the STR signal and provides control signals to the N-BIT SIGNAL CONVERTING UNIT and the LASER DRIVER.
- STRATEGY SIGNAL GENERATING UNIT (12):** Receives the STR signal and outputs a signal (S20) to the N-BIT SIGNAL CONVERTING UNIT.
- N-BIT SIGNAL CONVERTING UNIT (14):** Receives signals from the WAVEFORM CONTROL TABLE and the STRATEGY SIGNAL GENERATING UNIT. It outputs a signal (S10) to the D/A CONVERTER.
- D/A CONVERTER (20):** Receives the signal (S10) and outputs an analog signal (CK) to the LASER DRIVER.

Block 32 (Laser Output):

- LASER DRIVER (22):** Receives the analog signal (CK) and drives the LASER DIODE (LD).
- LD (LASER DIODE):** The output device of the system.

Signal Waveforms:

- S20:** A square wave signal.
- S12:** A signal waveform showing a sequence of pulses, labeled (D100), (D50), and (D50), with a '+' sign indicating a specific level.